

DesignStart – Introduction to Armv7-M

Summary:

This course provides a technical introduction to the Cortex-M3 DesignStart package. After introducing the Cortex-M3 DesignStart IP deliverables, it focuses on the Armv7-M programmers model and what tools can be used to develop software targeted for Cortex-M3 DesignStart, including an overview of the Cortex Microcontroller Software Interface Standard (CMSIS) and details on how to safely build code for optimal performance and memory footprint. The course also introduces the Armv7-M memory model and exception model and discusses some Armv7-M debug and trace solutions.

We will regularly be adding more content to this community to give you the opportunity to discuss with the subject matter experts.

Prerequisites:

- Read introductory information about DesignStart at: <https://developer.arm.com/products/designstart>
- Some knowledge of Armv7-M and/or Armv6-M is useful.
- Familiarity in embedded programming in C and assembler.
- Experience of embedded system development.

Audience:

This course is aimed at software developers who are looking to develop embedded applications with Arm DesignStart for Cortex-M3.

Delivery Method:

- Online

Length:

40 minutes